HTG12864I-25W-36C05-V10

产品名称（**Product name**）：黑白点阵 COG

型　　号（**Model**）：HTG12864I-25W-36C05-V10

编　　号（**Part number**）：

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深圳市鑫洪泰电子科技有限公司 **Shenzhen Hot Display Technology Co.,Ltd**		
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Table of Content

1. Bsaic Specifications ---------- 3
1.1 Display Specifications ---------- 3
1.2 Mechanical Specifications ---------- 3
1.3 Circuit Diagram ---------- 3
1.4 Terminal Function ---------- 4
1.5 Product Outline ---------- 5
1.6 Schematic Diagram ---------- 6
2. Absolute Maximum Ratings ---------- 7
3. Electrical Characteristics ---------- 7
3.1 DC Characteristics ---------- 7
3.2 LED Backlight Circuit ---------- 7
3.3 AC Characteristics ---------- 8
3.4 Reset Timing ---------- 10
4. Function specifications ---------- 11
4.1 The Parallel Interface ---------- 11
4.2 Basic Setting ---------- 12
4.3 Resetting the LCD module ---------- 12
4.4 Display Memory Map ---------- 12
4.5 Display Commands ---------- 13
4.6 Basic Operating Sequence ---------- 14
5. Inspection Standards ---------- 15
6. Handling Precautions ---------- 16
6.1 Mounting method ---------- 16
6.2 Cautions of LCD handling and cleaning ---------- 16
6.3 Caution against static charge ---------- 16
6.4 Packaging ---------- 16
6.5 Caution for operation ---------- 16
6.6 Storage ---------- 16
6.7 Safety ---------- 16
7 Packaging specifications ---------- 17

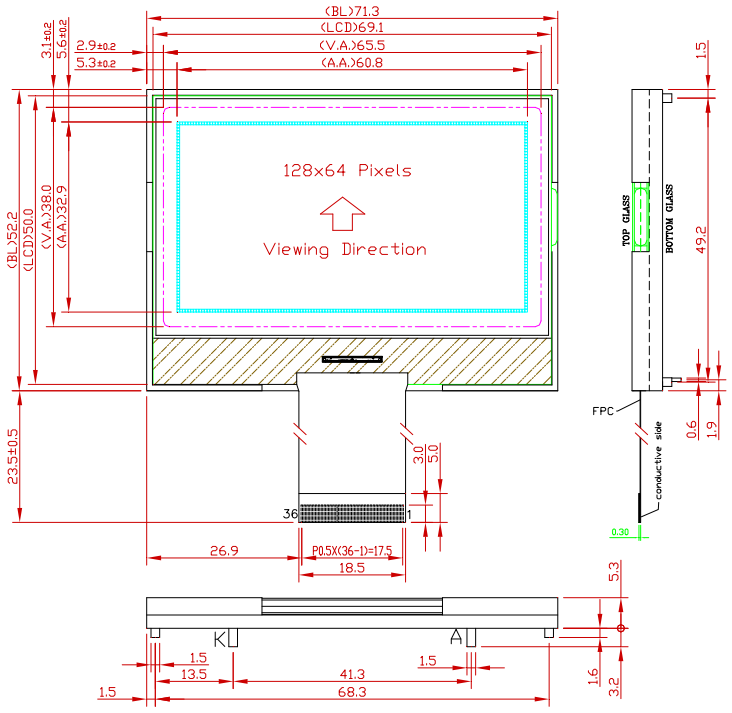
1. Bsaic Specifications

1.1 Display Specifications

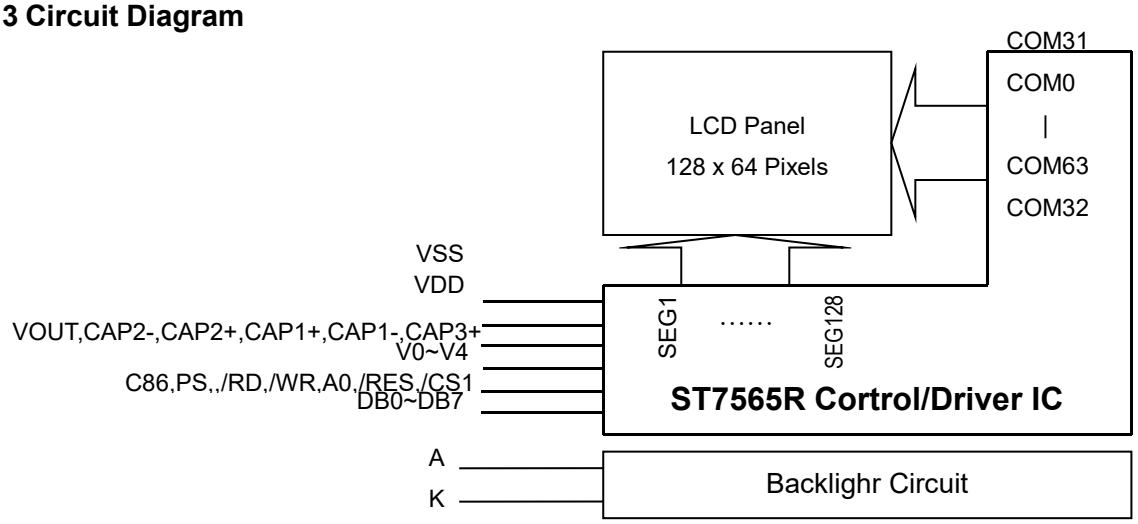
1>LCD Display Mode : STN-BLUE, Negative, Transmissive
2>Viewing Angle : 6H
3>Driving Method : 1/64 Duty, 1/9 Bias
4>Backlight : White LED

1.2 Mechanical Specifications (tolerance±0.3)

1>Outline Dimension : 71.3x 52.2 x 5.3mm (See attached Outline Drawing for Details)



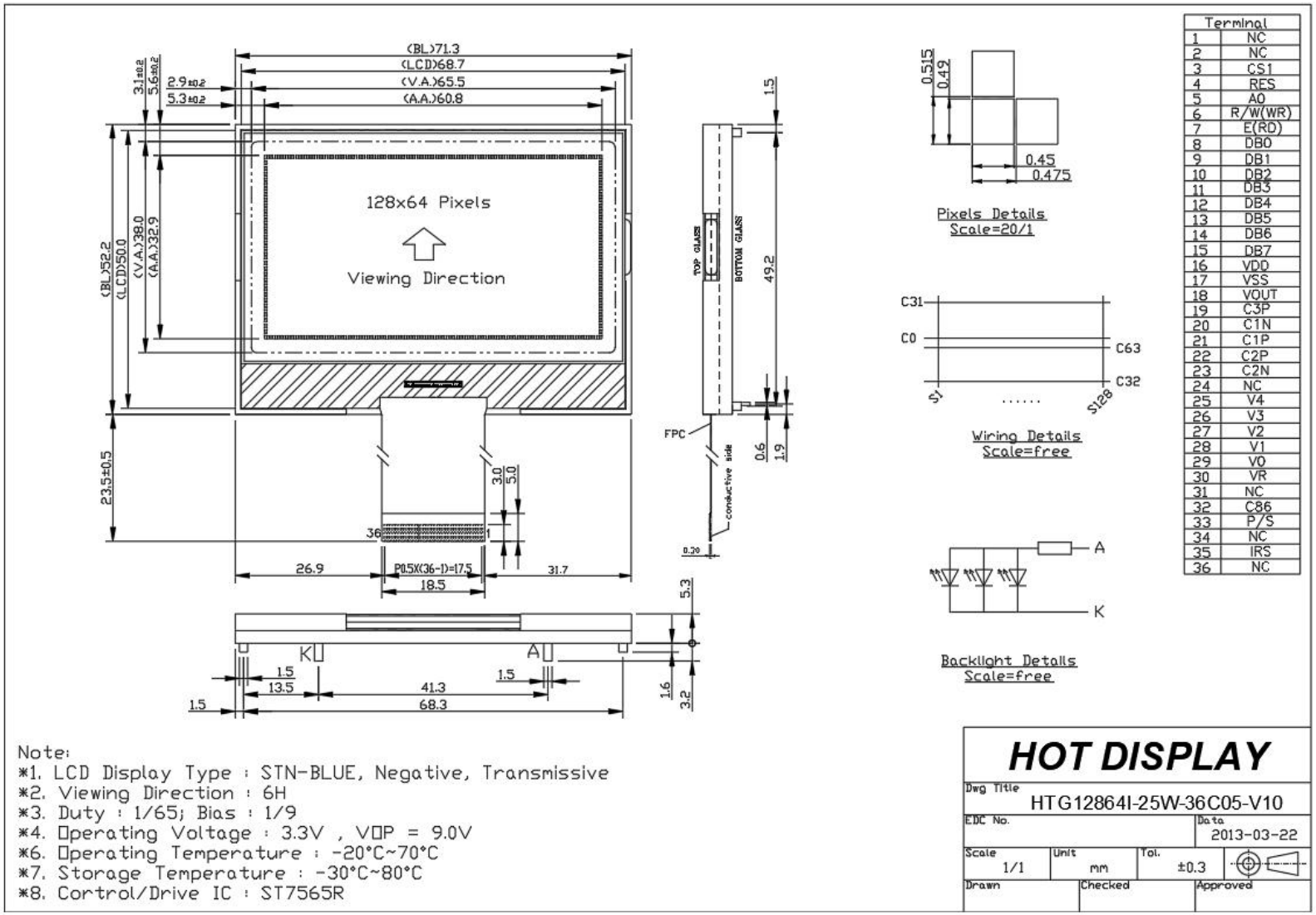
1.3 Circuit Diagram



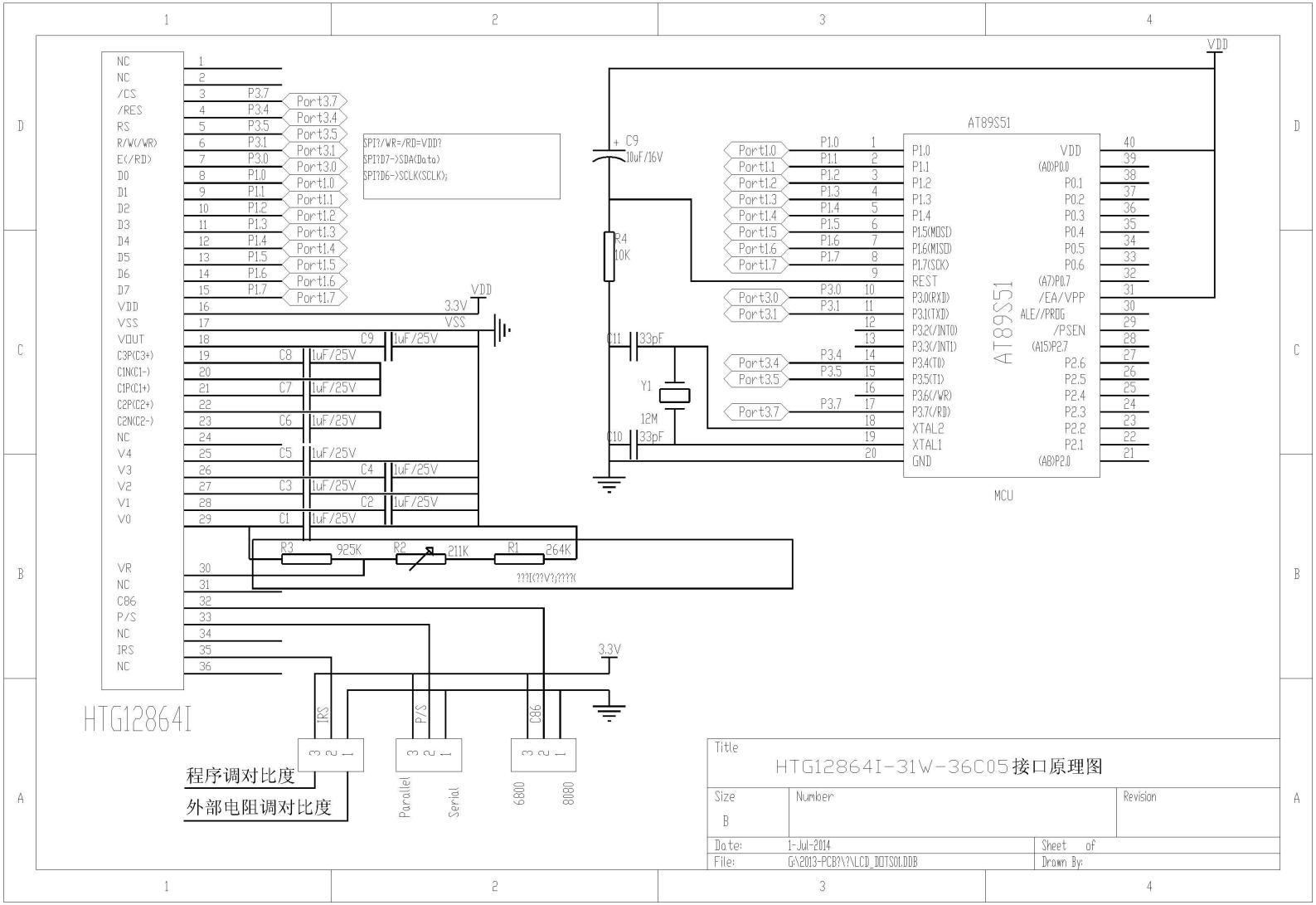
1.4 Terminal Function

<table>
<tr><th>Pin No.</th><th>Pin Name</th><th>Function</th></tr>
<tr><td>1~2</td><td>NC</td><td></td></tr>
<tr><td>3</td><td>/CS1</td><td>This is the chip select signal.</td></tr>
<tr><td>4</td><td>/RES</td><td>Rester Pin(L->H)</td></tr>
<tr><td>5</td><td>A0</td><td>A0 = “H”: Indicates that D0 to D7 are display data.
A0 = “L”: Indicates that D0 to D7 are control data.</td></tr>
<tr><td>6</td><td>/WR</td><td>Write (/WR) control signal input.</td></tr>
<tr><td>7</td><td>/RD</td><td>Read (/RD) control signal input.</td></tr>
<tr><td>8~15</td><td>DB0-DB7</td><td>8bit Date bus,
When the serial interface (SPI-4) is selected (P/S = “L”) :
D7 :serial data input (SI); D6 : the serial clock input (SCL).</td></tr>
<tr><td>16</td><td>VDD</td><td>Power supply voltage (Positive)</td></tr>
<tr><td>17</td><td>VSS</td><td>Negative power supply,0V</td></tr>
<tr><td>18</td><td>VOUT</td><td>DC/DC voltage converter.</td></tr>
<tr><td>19~23</td><td>C3+,C1-,C1+,C2+,C2-</td><td>DC/DC voltage converter</td></tr>
<tr><td>24</td><td>NC</td><td></td></tr>
<tr><td>25~29</td><td>V4,V3,V2,V1,V0</td><td>LCD driving voltages.</td></tr>
<tr><td>30</td><td>VR</td><td>Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider
IRS = “L” : the V0 voltage regulator internal resistors are not used.
IRS = “H” : the V0 voltage regulator internal resistors are used.</td></tr>
<tr><td>31</td><td>NC</td><td></td></tr>
<tr><td>32</td><td>C86</td><td>This is the MPU interface selection pin.
C86 = “H”: 6800 Series MPU interface.
C86 = “L”: 080 Series MPU interface.</td></tr>
<tr><td>33</td><td>P/S</td><td>This pin configures the interface to be parallel mode or serial mode.
P/S = “H”: Parallel data input/output.
P/S = “L”: Serial data input.
The following applies depending on the P/S status:
<table>
<tr><th>P/S</th><th>Data/Command</th><th>Data</th><th>Read/Write</th><th>4-line SPI Clock</th></tr>
<tr><td>“H”</td><td>A0</td><td>D0 to D7</td><td>/RD, /WR</td><td>X</td></tr>
<tr><td>“L”</td><td>A0</td><td>SI (D7)</td><td>Write only</td><td>SCL (D6)</td></tr>
</table>
When P/S = “L”, D0 to D5 must be fixed to “H”.
/RD (E) and /WR (R/W) are fixed to either “H” or “L”.
The serial access mode does NOT support read operation.</td></tr>
<tr><td>34</td><td>NC</td><td></td></tr>
<tr><td>35</td><td>IRS</td><td>IRS = “H”: Use the internal resistors
IRS = “L”: Do not use the internal resistors.</td></tr>
<tr><td>36</td><td>NC</td><td></td></tr>
</table>

1.5 Product Outline



1.6 Schematic Diagram



2. Absolute Maximum Ratings

Items	Symbol	MIN.	MAX.	Unit	Condition
Supply Voltage	VDD	-0.3	+3.6	V	Vss = 0V
Input Voltage	VIN	-0.3	VDD+0.3	V	Vss = 0V
Operating Temperature	TOP	-20	+70	℃	No Condensation
Storage Temperature	Tst	-30	+80	℃	No Condensation

3. Electrical Characteristics

3.1 DC Characteristics

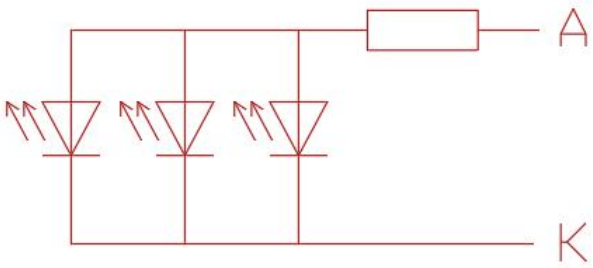
Vss = 0V,Top = 25℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Operating Voltage	VDD	3.0	3.3	3.6	V	VDD
Input High Voltage	VIH	0.8 x VDD	-	VDD	V	/CS1,/RES,A0,/WR, /RD,D0~D7,C86
Input Low Voltage	VIL	Vss	-	0.2 x VDD	V	
Output High Voltage	VOH	0.8 x VDD	-	VDD	V	D0~D7
Output Low Voltage	VOL	Vss	-	0.2 x VDD	V	D0~D7
Input Leakage Current	ILI	-1.0	-	1.0	μA	VDD
Output Leakage Current	ILo	-3.0	-	3.0	μA	VDD

3.2 LED Backlight Circuit

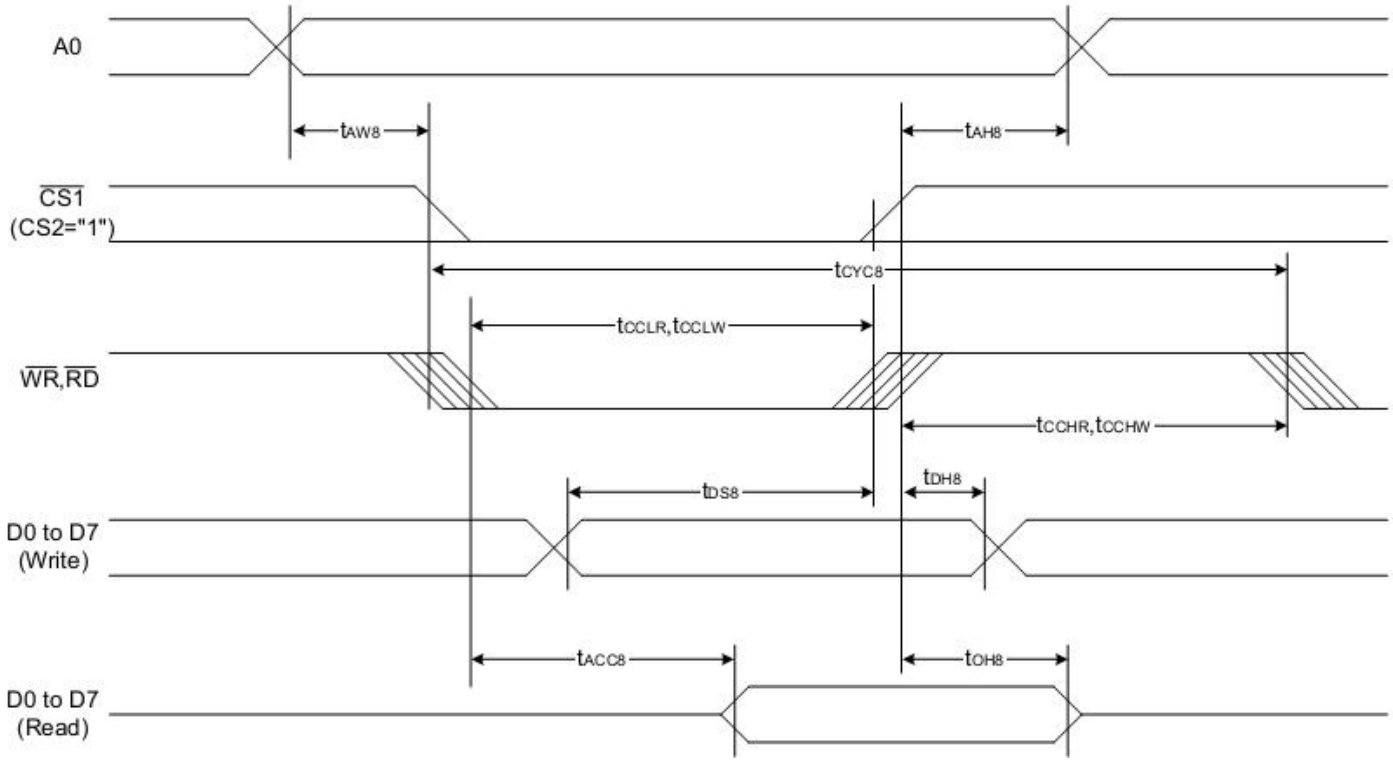
Vss = 0V,Top = 25℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forword Voltage	Vf BLA	-	3.0	-	V	VDD
Forword Current	If BLA	-	45	60	mA	VDD



3.3 AC Characteristics

3.3.1 8080 Mode System Bus Timing



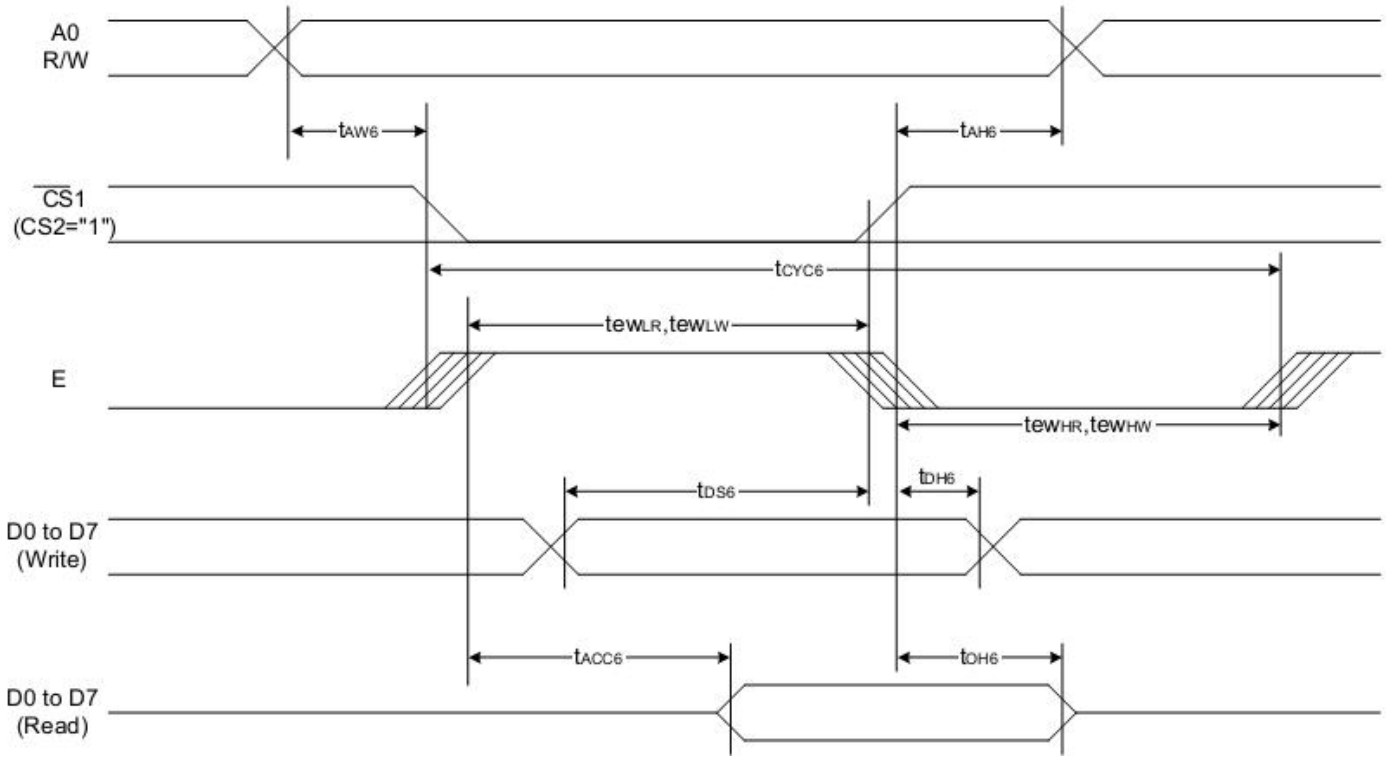
Vss = 0V,Top = 25℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
System cycle time	Tcyc8	500	-	-	ns	-
Address setup time(A0)	Taw8	10	-	-	ns	-
Address hold time(A0)	Tah8	10	-	-	ns	-
Control Low Pulse wide(/RD)	tcclr	275	-	-	ns	-
Control Low Pulse wide(/WR)	tcclw	275	-	-	ns	-
Control High Pulse wide(/RD)	tcchr	225	-	-	ns	-
Control High Pulse wide(/WR)	tcchw	225	-	-	ns	-
Data steup time	Tds8	50	-	-	ns	-
Data hold time	Tdh8	10	-	-	ns	-
/RD access time(*a)	Tacc8	-	-	200	ns	-
Output disable time(*a)	Tch8	15	-	150	ns	-

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

3.3.2 6800 Mode System Bus Timing



Vss = 0V,Top = 25℃

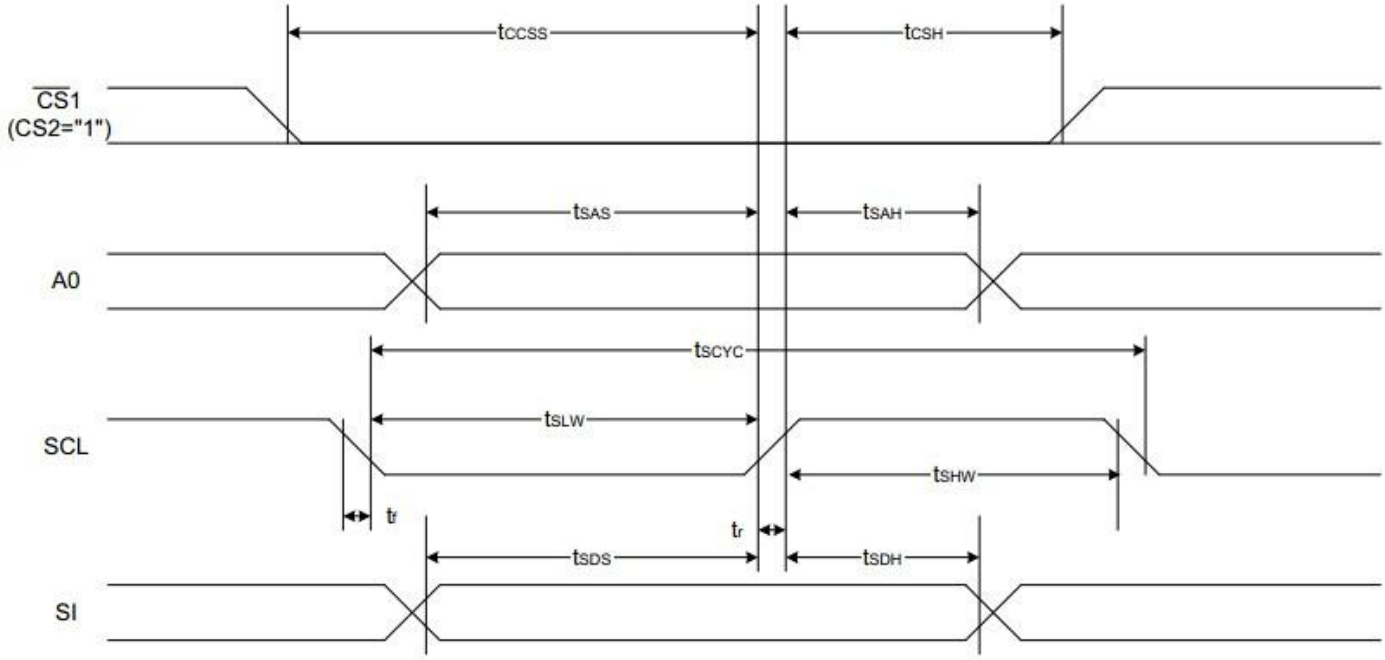
Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
System cycle time	Tcyc6	500	-	-	ns	-
Address setup time(A0)	Taw6	10	-	-	ns	-
Address hold time(A0)	Tah6	10	-	-	ns	-
Control Low Pulse wide(/RD)	tcclr	275	-	-	ns	-
Control Low Pulse wide(/WR)	tcclw	275	-	-	ns	-
Control High Pulse wide(/RD)	tcchr	225	-	-	ns	-
Control High Pulse wide(/WR)	tcchw	225	-	-	ns	-
Data steup time	Tds6	50	-	-	ns	-
Data hold time	Tdh6	10	-	-	ns	-
/RD access time(*a)	Tacc6	-	-	200	ns	-
Output disable time(*a)	Tch6	15	-	150	ns	-

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

***b. CL = 100pF**

3.3.3 4-line SPI Mode



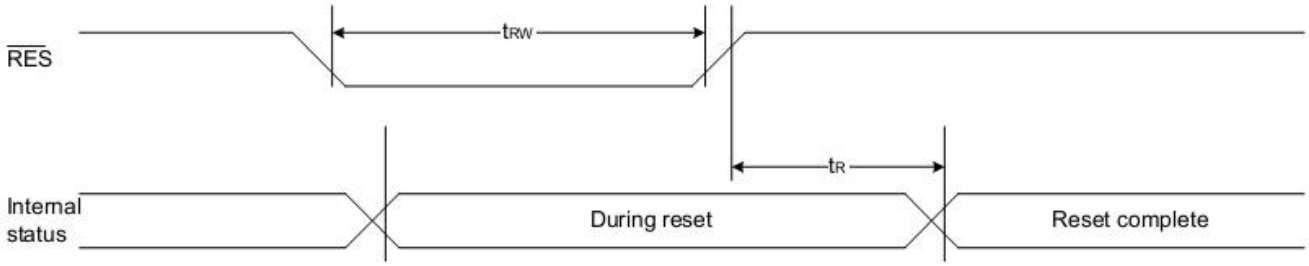
(V_{DD} = 3.3V, Ta = –30 to 85°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
4-line SPI Clock Period	SCL	Tscyc		50	—	ns
SCL "H" pulse width		Tshw		25	—	
SCL "L" pulse width		TSLW		25	—	
Address setup time	A0	TSAS		20	—	
Address hold time		Tsah		10	—	
Data setup time	SI	Tsds		20	—	
Data hold time		TSDH		10	—	
CS-SCL time	CS	Tcss		20	—	
CS-SCL time		Tcsh		40	—	

(V_{DD} = 1.8V, Ta = –30 to 85°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
4-line SPI Clock Period	SCL	TSCYC		200	—	ns
SCL "H" pulse width		TSHW		80	—	
SCL "L" pulse width		TSLW		80	—	
Address setup time	A0	TSAS		60	—	
Address hold time		TSAH		30	—	
Data setup time	SI	TSDS		60	—	
Data hold time		TSDH		30	—	
CS-SCL time	CS	TCSS		40	—	
CS-SCL time		TCSH		100	—	

3.4 Reset Timing



(VDD = 3.3V,Ta = –30 to 85°C)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	1.0	us
Reset "L" pulse width	/RES	tRW		1.0	—	—	us

Table 37

(VDD = 2.7V,Ta = –30 to 85°C)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	2.0	us
Reset "L" pulse width	/RES	tRW		2.0	—	—	us

Table 38

(VDD = 1.8V,Ta = –30 to 85°C)

Item	Signal	Symbol	Condition	Rating			Units
				Min.	Typ.	Max.	
Reset time		tR		—	—	3.0	us
Reset "L" pulse width	/RES	tRW		3.0	—	—	us

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

4. Function specifications

4.1 The Parallel Interface

Shared	6800 Mode		8080 Mode		Function
A0	R/W	E	/RD	/WR	
H	H	H	L	H	Reads the display data
H	L	H→L	H	L → H	Writes the display data
L	H	H	L	H	Staus read
L	L	H→L	H	L → H	Write Command data

NOTE: if SPI be used, C86=0,P/S=0;

4.2 Basic Setting

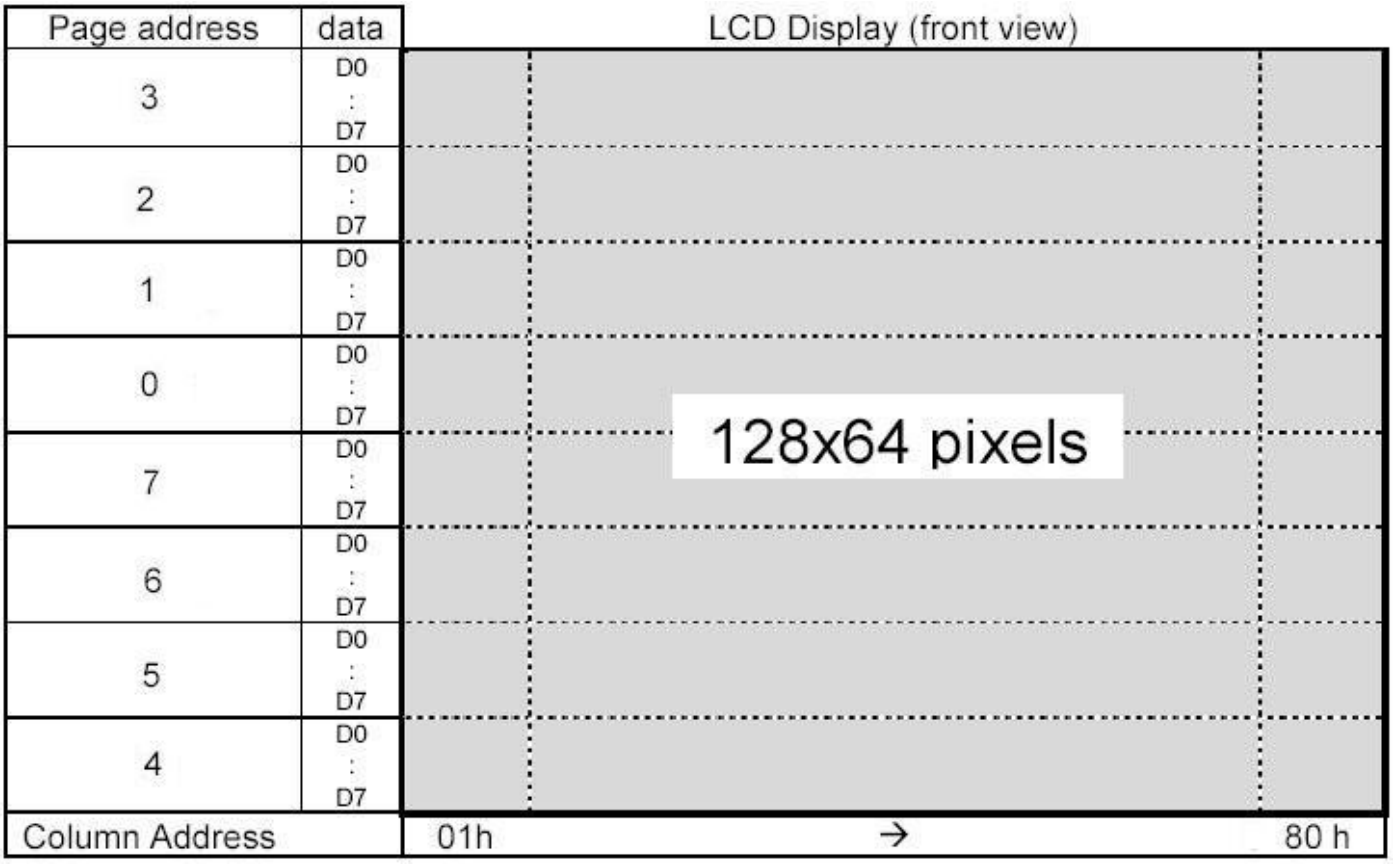
To drive the LCD module correctly and provide normally display, please use the following seting

1> ADC = 0 (normal)
2> SHL select = 1(reverse)
3> LCD Bias Select = 1/9
4> Initial Display Line = 0
5> Entire Display ON/OF = OFF(normal)
6> Reverse Display ON/OF = OFF(normal)
7> Set Power Control Set:
Voltage follower = ON,voltage converter = ON,Voltage regulator = ON
8> Display ON/OF =ON

4.3 Resetting the LCD module

The LCD module should be initialized bu using /RES terminal.
While turning on the VDD and VSS power supply, maintain /RES terminal at LOW level, After the Power supply stabilized, release the reset terminal(/RES = High)

4.4 Display Memory Map



4.5 Display Commands

No.	Instrctions	Code											Function
		A0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1	D0	
1	Display ON/OFF	0	1	0	1	0	1	0	1	1	1	DON	DON=0,display off DON=1,display on
2	Display start line set	0	1	0	0	1	Display start address						Set the display RAM display start line address
3	Set Page Address	0	1	0	1	0	1	1	Page address				Set the display RAM Page address
4	Ser Column Address (Upper-4 bits)	0	1	0	0	0	0	1	Col. Add				Set the upper-4-bit of column address counter
	Ser Column Address (Lower-4 bits)	0	1	0	0	0	0	0	Col. Add				Set the low-4-bit of column address counter
5	Read Staus	0	0	1	Status				0	0	0	0	Read the status data
6	Write Display Data	1	1	0	Write Data								Write data into the display RAM
7	Read Display Data	1	0	1	Read Data								Read data from the display RAM
8	ADC Select	0	1	0	1	0	1	0	0	0	0	ADC	Set the display RAM address SEG output Correspondence ADC = 0,Normal. ADC = 1,Reverse
9	Normal/Reverse Display	0	1	0	1	0	1	0	0	1	1	REV	REV = 0, Normal REV = 1, Reverse
10	Entire Display ON/OFF	0	1	0	1	0	1	0	0	1	0	EON	EON = 0, Normal EON = 1, Entire display ON
11	Set LCD Bias	0	1	0	1	0	1	0	0	0	1	BIAS	Bias = 0, 1/9 Bias Bias = 1, 1/7 Bias
12	Set Read-Modify-Write	0	1	0	1	1	1	0	0	0	0	0	Enter the “Read-Modify-Write” mode
13	Reset Read-Modify-Write	0	1	0	1	1	1	0	1	1	1	0	Clear the “Read-Modify-Write” mode
14	Reset	0	1	0	1	1	1	0	0	0	1	0	Resets the LCD module
15	SHL S elect	0	1	0	1	1	0	0	SHL	*	*	*	Set the COM scanning direction SHL = 0, Normal SHL = 1, Flipped in y-direction * = don’t care terms
16	Power Control Set	0	1	0	0	0	1	0	1	VC	VR	VF	Set the power circuit operation mode VF : LCD Supply Voltage Follower VR : LCD Supply Voltage Regulator VF : LCD Supply Voltage Converter (1 = ON, 0 = OFF)
17	Regulator Resistor Select	0	1	0	0	0	1	0	0	Ratio			Set the built-in resistor ratio (Rb/Ra)
18	Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	Set reference voltage mode
	Electronic volume register set	0	1	0	*	*	Electronic Control value						Set reference voltage register
19	Power Save		-	-	-	-	-	-	-	-	-	-	Compound instruction Display OFF + Entire Display ON
20	NOP	0	1	0	1	1	1	0	0	0	1	1	Non-operation command

Note:

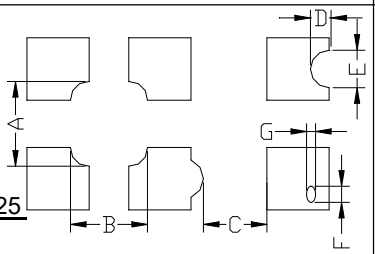
***a. For the details of the Display Commands, please refer to ST7565R data sheet**

4.6 Basic Operating Sequence

Initialization Sequence

	A0	D7	D6	D5	D4	D3	D2	D1	D0	hex	Note
Turn on Power Supply VDD & VSS While maintaining /RES at LOW	-	-	-	-	-	-	-	-	-	-	-
Wait until power supply is stabilized	-	-	-	-	-	-	-	-	-	-	-
Release the /RES Reset Signal (/RES = High)	-	-	-	-	-	-	-	-	-	-	See AC Characteristics section for timing details
LCD Bias = 1/9	0	1	0	1	0	0	0	1	0	A2H	LCD Characteristics
ADC = Normal	0	1	0	1	0	0	0	0	0	A0H	No flip on x-direction (SEG)
SHL = Reverse	0	1	1	0	0	1	0	0	0	C8H	Flip on y- direction (COM)
Initial Display Line = 0	0	0	1	0	0	0	0	0	0	40H	i.e. Display RAM “Page 0-D0” Matched to top line of the LCD
Power Control Voltage Follower = OFF Voltage Regulator = OFF Voltage Converter = ON	0	0	0	1	0	1	1	0	0	2CH	Turn on the internal Voltage Converter and wait until VOUT stable
Delay 50ms	-	-	-	-	-	-	-	-	-	-	
Power Control Voltage Follower = OFF Voltage Regulator = OFF Voltage Converter = ON	0	0	0	1	0	1	1	1	0	2EH	Turn on the internal Voltage Regulator and wait until VOUT stable
Delay 50ms	-	-	-	-	-	-	-	-	-	-	
Power Control Voltage Follower = OFF Voltage Regulator = OFF Voltage Converter = ON	0	0	0	1	0	1	1	1	1	2FH	Turn on the internal Voltage Follower and wait until VOUT stable
Delay 50ms	-	-	-	-	-	-	-	-	-	-	
Regulator Resistor Select	0	0	0	1	0	0	1	0	1	25H	Set the built-in resistor ratio to middle
Set Reference Voltage Mode	0	1	0	0	0	0	0	0	1	81H	Set to the middle of the range it may be adjused
Set Reference Voltage Resistor	0	0	0	0	1	1	0	1	0	1AH	For achieving the best display contrast
Display ON	0	1	0	1	0	1	1	1	1	AFH	Turn on the LCD display
Set Page Address = 0	0	1	0	1	1	0	0	0	0	B0H	Specify the display data RAM page address to 00H
Set Column Address (Upper -4bit = 0)	0	0	0	0	1	0	0	0	0	10H	Specify the display data RAM column address to 00H
Set Column Address (Lower-4bit =4)	0	0	0	0	0	0	1	0	0	01H	
Write Display Data	1	Display Data									-
Write Other Display Data											

5. Inspection Standards

Item	Criterion for defects	Defect type
1) Display on inspection	(1) Non display (2) Vertical line is deficient (3) Horizontal line is deficient (4) Cross line is deficient	Major
2) Black / White spot	Size Φ(mm) Acceptable number Φ≤0.3 Ignore (note) 0.3<Φ≤0.45 3 0.45<Φ≤0.6 1 0.6<Φ 0	Minor
3) Black / White line	Length (mm) Width (mm) Acceptable number L≤10 W≤0.03 Ignore 5.0≤L≤10 0.03<W≤0.04 3 5.0≤L≤10 0.04<W≤0.05 2 1.0≤L≤10 0.05<W≤0.06 2 1.0≤L≤10 0.06<W≤0.08 1 L≤10 0.08<W follows 2) point defect Defects separate with each other at an interval of more than 20mm	Minor
4) Display pattern	 $\frac{A+B}{2}\leq 0.28$ | $0<C$ | $\frac{D+E}{2}\leq 0.25$ | $\frac{F+G}{2}\leq 0.25$ Note: 1) Up to 3 damages acceptable 2) Not allowed if there are two or more pinholes every three-fourth inch.	Minor
5) Spot-like contrast irregularity	Size Φ(mm) Acceptable Number Φ≤0.7 Ignore (note) 0.7<Φ≤1.0 3 1.0<Φ≤1.5 1 1.5<Φ 0 Note: 1) Conformed to limit samples. 2) Intervals of defects are more than 30mm.	Minor
6) Bubbles in polarizer	Size Φ(mm) Acceptable Number Φ≤0.4 Ignore (note) 0.4<Φ≤0.65 2 0.65<Φ≤1.2 1 1.2<Φ 0	Minor
7) Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with “2) Black/white spot”, and “3) Black/White line”.	Minor
8) Stains on the surface of LCD panel	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning.	Minor
9) Rainbow color	No rainbow color is allowed in the optimum contrast on state within the active area.	Minor
10) Viewing area encroachment	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor
11) Bezel appearance	Rust and deep damages that are visible in the bezel are rejected.	Minor
12) Defect of land surface contact	Evident crevices that are visible are rejected.	Minor
13) Parts mounting	(1) Failure to mount parts (2) Parts not in the specifications are mounted (3) For example: Polarity is reversed, HSC or TCP falls off.	Minor
14) Part alignment	(1) LSI, IC lead width is more than 50% beyond pad outline. (2) More than 50% of LSI, IC leads is off the pad outline.	Minor
15) Conductive foreign matter (solder ball, solder hips)	(1) 0.45<Φ, N≥1 (2) 0.3<Φ≤0.45, N≥1, Φ: Average diameter of solder ball (unit: mm) (3) 0.5<L, N≥1, L: Average length of solder chip (unit: mm)	Minor
16) Bezel flaw	Bezel claw missing or not bent	Minor
17) Indication on name plate (sampling indication label)	(1) Failure to stamp or label error, or not legible.(all acceptable if legible) (2) The separation is more than 1/3 for indication discoloration, in which the characters can be checked.	Minor

6. Handling Precautions

6.1 Mounting method

A panel of LCD module made by our company consists of two thin glass plates with polarizers that easily get damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board (PCB), extreme care should be used when handling the LCD modules.

6.2 Cautions of LCD handling and cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

-Isopropyl alcohol

-Ethyl alcohol

-Trichlorotriflorothane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

-Water

-Ketene

-Aromatics

6.3 Caution against static charge

The LCD module use C-MOS LSI drivers. So we recommend you:

Connect any unused input terminal to V_{dd} or V_{ss}. Do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

6.4 Packaging

-Module employs LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

-To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

6.5 Caution for operation

-It is an indispensable condition to drive LCD module within the limits of the specified voltage since the higher voltage over the limits may cause the shorter life of LCD module.

-An electrochemical reaction due to DC (direct current) causes LCD undesirable deterioration so that the uses of DC (direct current) drive should be avoided.

-Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD module may show dark color in them. However those phenomena do not mean malfunction or out of order of LCD module, which will come back in the specified operating temperature.

6.6 Storage

In the case of storing for a long period of time, the following ways are recommended:

-Storage in polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with not desiccant.

-Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping the storage temperature range.

-Storing with no touch on polarizer surface by any thing else.

6.7 Safety

-It is recommendable to crash damaged or unnecessary LCD into pieces and to wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

-When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well at once with soap and water.

7. Packaging Specifications

鑫洪泰 HOT DISPLAY

Packaging Specifications
HTG12864I

Approved	Checked	Designed

6.1 Packaging Material

No	Item	Dimensions(mm)	1PCS Weight(KG)	Quantity	Total Weight	
1	COG	71.2*52.2*5.5	0.032	50	1.6	
2	PE Bag	80*80	0.001	200	0.352	
3	Foam Rubber Cushion	310*170	0.0175	12	0.21	
4	Partition A1	310*170*100	0.30	6	1.8	
5	Product Box	330*180*120(neutral packing)	0.45	6	2.7	
6	Carton	480*390*330(neutral packing)	1.0	1	1.0	
7	Tape			AR		
8	Label Specifications			1		
9	Label Rohs			1		
10	Label ESD			1		

6.2. Total LCD Weight in carton: 7.7 KG±10%

6.3. Packaging Specifications and Quantity:

(1) Quantity Of Spacer:A1*8

(2) Total LCM quantity in carton: quantity per box 50* no of boxes 6 = 300

